



AU85X2/X4 Demo Board Design Brief

1 Introduction

This design is to demo our AU85X2 and AU85X4 chip. It supports the ATSC, NTSC, Composite, S-video, IIS output and J83B (AU852X only), PAL (AU8522 only with 8MHz tuner) and FM radio (AU8522 only).

2 Hardware Configuration

2.1 Major Chip Set:

No.	Function	Name/Module	Manufactory/Data Sheet	Provider	Remark
1	ATSC/NTSC etc. Decoder	AU8502/AU8504 AU8522/AU8524	Auvitek/ \zeus\marketing\data sheet\	Auvitek.	
2	Tuner	DVT-6A/W41F2	XuGuang	XuGuang	
3	Tuner	DTZ1/W400	QingJia	QingJia	
4	IIS Interface.	CS5340	Cirrus Logic	Cirrus Logic	

2.2 Functions List:

No.	Function	Details	Remark
1	ATSC Decoder	RF Input	
2	Tuner Setting	Support two tuners	
3	NTSC RF Input	RF Input	
4	Composite Input		
5	QAM64 Input		AU852X
6	QAM256 Input		AU852X
7	PAL_BG RF Input		AU852X
8	PAL_DK RF Input		AU852X
9	PAL_I RF Input		AU852X
10	S-video Input		
11	FM Radio		AU852X
12	Audio Decoder		
13	IIS Output		
14	Remote Controller		

3 Hardware Support

No.	Items	Version No.	Remark
1	Version No.	Ver. 2.00	
2	BOM	Ver. 2.00	
3	Schematic	Ver. 2.00	
4	GBR	Ver. 2.00	
5	PCB	Ver. 2.00	

4 Software Support

No.	Software Support	Version No.	Remark
1	Driver version		
2	I2C Controller		