

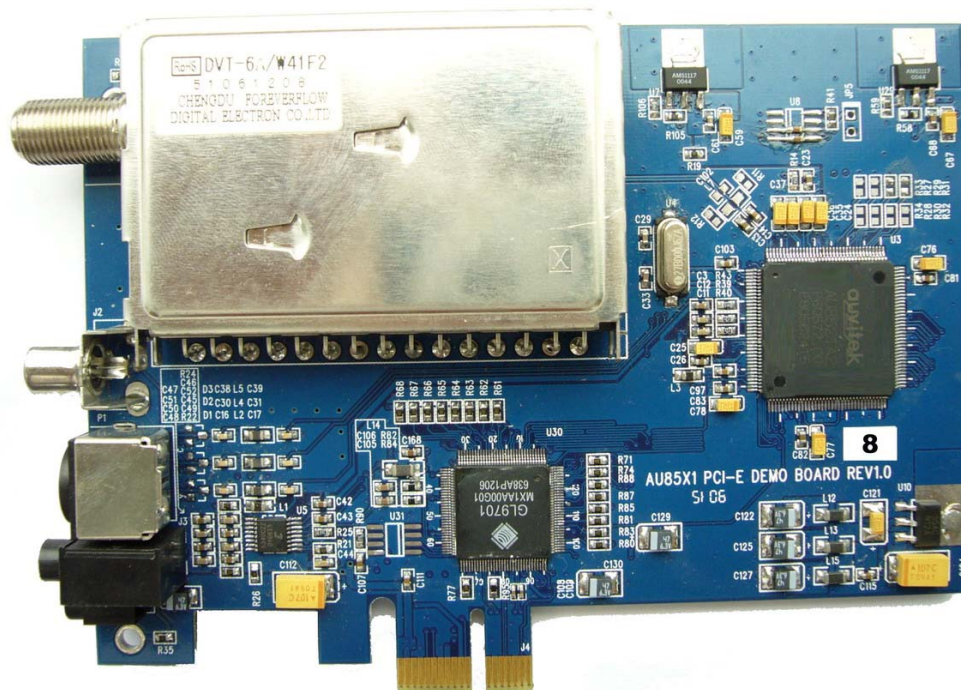


AU85X1 PCI-E Demo Board

Design Brief

1 Introduction

This board will be used to demo our AU8501 and AU8521 chip. It supports the ATSC, NTSC, Composite, S-video, IIS input and J83B (AU8521 only), PAL (AU8521 only with 8MHz tuner) and FM radio (AU8521 only).



2 Hardware Configuration

2.1 Major chip set:

No.	Function	Name/Module	Manufactory/Data sheet	Provider	Remark
1	ATSC/NTSC etc. Decoder	AU8501/AU8521	Auvitek/ \zeus\marketing\data sheet\	Auvitek	
2	XuGuang Tuner		XuGuang	XuGuang	
3	Regulators	AT24C02			
4	IIS Interface	CS5340			
5	PCI-E Bridge	GL9701	Genesys Logic Inc.	Genesys Logic Inc.	

2.2 Functions List:

No.	Function	Details	Remark
1	ATSC Decoder	RF Input	
2	Tuner Setting	Support four tuners	
3	NTSC RF Input	RF Input	
4	Composite Input		
5	QAM64 Input		AU8521 only
6	QAM256 Input		AU8521 only
7	PAL BG RF Input		AU8521 only
8	PAL DK RF Input		AU8521 only
9	PAL I RF Input		AU8521 only
10	S-video Input		
11	FM Radio		AU8521 only
12	Audio Decoder		
13	IIS Input		
14	Remote Controller		

3 Hardware support

No.	Items	Version No.	Remark
1	Version No.	Ver. 2.00	
2	BOM	Ver. 2.00	
3	Schematic	Ver. 2.00	
4	GBR	Ver. 2.00	
5	PCB	Ver. 2.00	

4 Software support

No.	Software support	Version No.	Remark
1	Driver version	Ver. 1.00	
2	I2C Controller	NA	